

टाटा मूलभूत अनुसंधान संस्थान हैदराबाद
TATA INSTITUTE OF FUNDAMENTAL RESEARCH HYDERABAD
प्लाट नं. ३६/पी, गोपनपल्ली ग्राम, सेरिलिंगमपल्ली मंडल, रंगा रेड्डी जिला,
हैदराबाद - ५०० १०७, तेलंगाना.
Plot No.36/P, Gopanpally Village, Serilingampally Mandal, Ranga Reddy District,
Hyderabad - 500 107, Telangana, India.

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वेबसाइट /Website: www.tifrh.res.in

ई-मेल /Email: purchasegroup@tifrh.res.in
तिथि /Date: 28-05-2024

Public Tender Ref. No. TIFR/PD/CA24-02/24-25/M/02

Sub: CORRIGENDUM NO. 1, against Tender Ref. No. TIFR/PD/CA24-02/24-25/M/02 for Supply, Installation and Commissioning of High-Performance Computing (HPC) Cluster for TIFR Hyderabad.

Dear Bidders/Vendors,

Please refer to the subject tender published on 16-05-2024, the following corrigendum to the subject is being issued:

S. No	Change from	To be amended as
1	<p>Annexure-A</p> <p>Page No – 3, Point no -1</p> <p>The bidders should have minimum three successful installations at Government/ Public/ Private Research organizations /institutions /Manufacturing industries using architecture and technologies to those being proposed in their quotation against this tender across the country within the last five years.</p> <p>In addition, the following condition should also be satisfied.</p> <p><input type="checkbox"/> At least one installation of minimum 80% or</p> <p><input type="checkbox"/> At least two installations of each 60% or</p> <p><input type="checkbox"/> At least three installations of each 40% of the tender value</p> <p>Purchase order with final commissioning report with the mention of compute architecture details duly signed by customer should be Submitted with the technical bid.</p> <p>The contact details of the Customer's technical person should be provided. TIFR-H may independently contact them to verify the claim</p>	<p>The bidders/OEM should have minimum three successful installations at Government/ Public/ Private Research organizations /institutions /Manufacturing industries using architecture and technologies to those being proposed in their quotation against this tender across the country within the last five years.</p> <p>In addition, the following condition should also be satisfied.</p> <p><input type="checkbox"/> At least one installation of minimum 80% or</p> <p><input type="checkbox"/> At least two installations of each 60% or</p> <p><input type="checkbox"/> At least three installations of each 40% of the tender value</p> <p>Purchase order with final commissioning report with the mention of compute architecture details duly signed by customer should be Submitted with the technical bid. The contact details of the Customer's technical person should be provided. TIFR-H may independently contact them to verify the claim</p> <p>Note: The bidders qualifying through OEM's installation references should provide complete comprehensive hardware and software support</p>

		<p>through OEM only. The SLA agreement for the same should be signed by the OEM and OEM should confirm this in their letterhead.</p>
<p>2</p>	<p>Page No – 5 point - a</p> <p>The OEM/bidder should carry out below listed benchmark programs on 10 TF, 30 TF and 50 TF peak performance configurations of the proposed solution and also produce the extrapolated outputs of the 100 TF, 200 TF and fully offered solution in peak performance configurations. It is not necessary to run the benchmark codes on the same quoted processor mentioned in the tender. However, the produced results should match with the results of the offered solution as a part of the acceptance test. The maximum allowable deviation from the extrapolated results in the acceptance test should be less than 3%. The results (with TFLOP and core count where applicable) should be presented in an output file and included in the technical bid.</p> <ul style="list-style-type: none"> • High Performance Linpack (HPL) Benchmark should be provided for the above mentioned configurations and the same will be considered for technical evaluation. The minimum required performance for technical qualification is 80%. • Other applications for Benchmark – LAMMPS, P3DFFT, GROMACS. (https://www.tifrh.res.in/webdata/documents/events/tenders/2024/mar/bm2024.zip) <p>Note: All the above benchmark codes including HPL should be run using open source gnu compilers 9.0 or above, openmpi 4.0 or above. Other commercial compilers or any code optimizations are not allowed for benchmarking.</p> <p>Provide read-only output of the above benchmark on DVD/CD along with the bid. Do not provide the print out of the outputs. Proposals of vendors who do not fulfil the above criteria or who fail to submit documentary proof would be rejected.</p>	<p>The OEM/bidder should carry out below listed benchmark programs on 9/10 TF, 18/20 TF and 36/50 TF peak performance configurations of the proposed solution and also produce the extrapolated outputs of the 100 TF, 200 TF and fully offered solution in peak performance configurations. It is not necessary to run the benchmark codes on the same quoted processor mentioned in the tender. However, the produced results should match with the results of the offered solution as a part of the acceptance test. The maximum allowable deviation from the extrapolated results in the acceptance test should be less than 3%. The results (with TFLOP and core count where applicable) should be presented in an output file and included in the technical bid.</p> <ul style="list-style-type: none"> • High Performance Linpack (HPL) Benchmark should be provided for the above mentioned configurations and the same will be considered for technical evaluation. The minimum required performance for technical qualification is 80%. • Other applications for Benchmark – LAMMPS, P3DFFT, GROMACS. (https://www.tifrh.res.in/webdata/documents/events/tenders/2024/mar/bm2024.zip) <p>Note: All the above benchmark codes including HPL should be run using open source gnu compilers 9.0 or above, openmpi 4.0 or above. If the bidder wishes to use any compiler other than open source GNU and OpenMPI, bidder has to submit the benchmark results for both GNU and the other compiler. The technical evaluation will be made on GNU compilers performance results. Bidder can bid with the commercial compiler if they wish; however, no weightage will be given during commercial evaluation. If the bidders provide benchmark results using only commercial compilers, then they should provide the same compilers at no extra cost along with the cluster and provide support throughout the warranty period.</p>

		Provide read-only output of the above benchmark on DVD/CD along with the bid. Do not provide the print out of the outputs. Proposals of vendors who do not fulfil the above criteria or who fail to submit documentary proof would be rejected.
3	Page No – 17 Delivery time: Delivery period will be 30 weeks from the date of purchase order. Once delivered to onsite, the installation, commissioning and acceptance testing period will be within 4 weeks from the date of delivery of equipment. Delay in delivery will have penalty. TIFR-H reserves the right to cancel the order if it is not deployed even after that.	Delivery time: Delivery period will be 40 weeks from the date of purchase order. Once delivered to onsite, the installation, commissioning and acceptance testing period will be within 4 weeks from the date of delivery of equipment. Delay in delivery will have penalty. TIFR-H reserves the right to cancel the order if it is not deployed even after that.
4	Page No – 23 Point No- 26 Delivery Period: Within 30 weeks from the date of release of Purchase Order.	Delivery Period: Within 40 weeks from the date of release of Purchase Order.

Note: Remaining technical specifications, terms & conditions as per tender only.

All other terms & conditions of subject tender shall remain unchanged. This Corrigendum No. 01 is an integral part of the subject tender and a copy of the same must be submitted along with the offer duly signed and stamped.

**प्रशासनक अवधकारी / ADMINISTRATIVE OFFICER
(क्रय अनुभाग) / (PURCHASE SECTION)
टीआईएफआर हैदराबाद / TIFR HYDERABAD**